SEP 0 6 2005

IN THE U.S. PATENT AND TRADEMARK OFFICE

re U.S. Parent Application of:

APPLICANTS: Antti Ruha et al.

SERIAL NO.: 10/005,766

FILING DATE: 11/02/2001

EXAMINER: Pablo N. Tran ART UNIT: 2685

ATTORNEY'S DOCKET NO.: 872.0100.U1(US)

TITLE:

MULTI-MODE I/O CIRCUITRY SUPPORTING LOW INTERFERENCE

SIGNALING SCHEMES FOR HIGH SPEED DIGITAL INTERFACES

Commissioner for Patents

Alexandria, VA 22313

RESPONSE TO OFFICE ACTION

Sir:

This paper is herewith filed in response to the Examiner's Office Action mailed on 07/05/2005 for the above-captioned U.S. Patent Application. As such, no petition for an extension of time or fee is believed to be due, as this response is herewith filed within the shortened statutory period. However, should the undersigned attorney be mistaken, please consider this a petition for any extension of time that may be required to maintain the pendency of this Patent Application, and charge deposit account no.: 50-1924 for any required fee deficiency.

Please amend the application as shown below.